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STATEMENT UNDER 37 CFR 3.73(b)		
Applicant/Patent Owner: Richard S. Roy		
•		Filed/Issue Date: 04/26/2005
Titled: SIMULTANEOUS ACCESS AND (CACHE LOADING I	N A HIERARCHICALLY ORGANIZED MEMORY CIRCUIT
Synopsys, Inc.	_{, a} Delawa	re corporation
(Name of Assignee)	(Type of	Assignee, e.g., corporation, partnership, university, government agency, etc.
states that it is:		
1. X the assignee of the entire right, title, a	and interest in;	
2. an assignee of less than the entire rig (The extent (by percentage) of its own		
3. the assignee of an undivided interest	in the entirety of (a co	omplete assignment from one of the joint inventors was made)
the patent application/patent identified above, by	virtue of either:	
A. An assignment from the inventor(s) o the United States Patent and Tradem copy therefore is attached.	of the patent application nark Office at Reel	n/patent identified above. The assignment was recorded in, Frame, or for which a
OR		
	the patent application	n/patent identified above, to the current assignee as follows:
1. From: Richard S. Roy		To: Virage Logic Corporation
	ed in the United States , Frame <u>0341</u>	s Patent and Trademark Office at, or for which a copy thereof is attached.
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As required by 37 CFR 3.73(b)(1)(i), the or concurrently is being, submitted for reco		e of the chain of title from the original owner to the assignee was, 7 CFR 3.11.
		nment document(s)) must be submitted to Assignment Division in a records of the USPTO. <u>See</u> MPEP 302.08]
The undersigned (whose title is supplied below) i	is authorized to act on	behalf of the assignee.
/Dohyun Ahn/		March 11, 2011
Signature		Date
Dohyun Ahn, Reg. No. 63,237		Attorney for Assignee
Printed or Typed Name		 Title

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